

Amendments to the Specification:

Please replace the paragraph beginning at page 40, line 17 with the following amended paragraph:

Fig. 7B is a photograph of a shift **[[resistor]]** register for reference. The shift **[[resistor]]** register circuit shown in Fig. 7B is one of important circuits constituting an experimentally manufactured peripheral drive circuit, and is a logic circuit for specifying an address of a pixel region. Especially, a shift **[[resistor]]** register circuit for horizontal scanning (source side use) is required driving at very high frequency of about several MHz to several tens MHz at a real operation.